

Figure 1: Schematic diagram of the experimental setup. The diagram shows a horizontal timeline of events. At the bottom, a timeline starts at 40s, followed by a 12s interval, then a 250 L interval, then a 1760 L interval, then a 210 L interval, then a 11x250 L interval, and finally an 80 L interval. Above this, three horizontal bars represent different experimental conditions. The top bar is labeled '28' 2#1/2 L=3170 and has a duration of 2980. The middle bar is labeled '29' 2#1/0 L=2980 and has a duration of 2980. The bottom bar is labeled '28' 2#1/2 L=3170 and has a duration of 2980. The bars are connected by vertical lines at the 210 L and 11x250 L points.

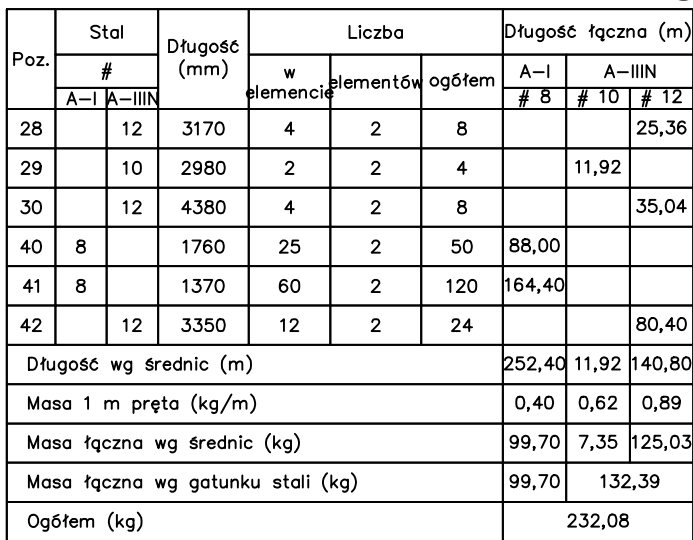
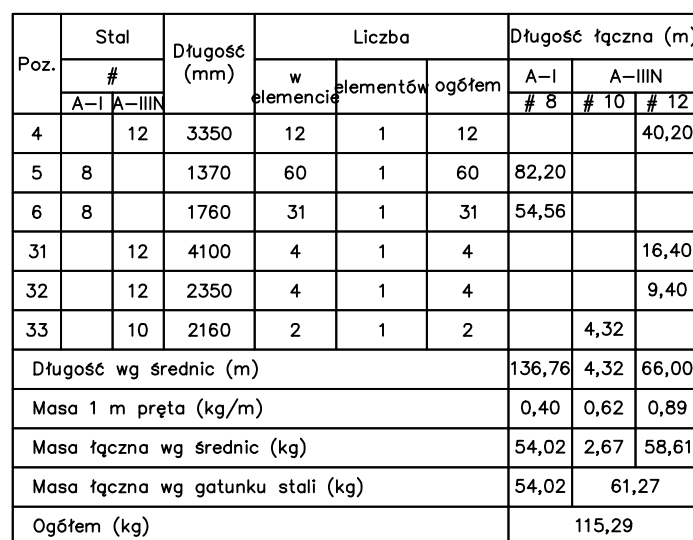



Diagram illustrating a 32-bit bus system configuration. The bus is divided into two 16-bit channels. The top channel is labeled **32 2#12 L=2350** and the bottom channel is labeled **32 2#12 L=2350**. Both channels have a **2160** label above them. The diagram is labeled **16** on the left and **16** on the right.



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